

## INVITATION FOR QUOTATION

**Package Code: TEQIP/PROJ88/04-05**

**Current Date: 30-Jan., 2020**

**Method: Direct Contacting**

**To,**

CoreEL Technologies,

DK House, T-93, 3<sup>rd</sup> Floor, Behind Yusuf Sarai Community Centre,

Gautam Nagar, New Delhi – 110049.

**Sub: Invitation for Quotations for supply of Goods**

Dear Sir,

1. You are invited to submit your most competitive quotation for the following goods with item wise detailed specifications given at Annexure I,

Sr. No	Brief Description	Quantity	Delivery Period (In days)	Place of Delivery	Installation /Training Requirement (if any)
1	Vivado Design Suit	01 Bundle	60 days	FOR Pantnagar	Yes
2	Xilinx FPGA Kit	02	60 days	FOR Pantnagar	Yes

2. Government of India has received a credit from the International Development Association (IDA) towards the cost of the **Technical Education Quality Improvement Programme [TEQIP]-Phase III** Project and intends to apply part of the proceeds of this credit to eligible payments under the contract for which this invitation for quotations is issued.
3. **Qualification Criteria: The bidder/ supplier should have**
- A minimum of 3 year's experience of supplying similar items.
  - A turnover of Rs. 50 lacs at least once in last three years.
  - Not been blacklisted by any Govt. Institution/ Organization
4. Quotation.
- The contract shall be for the full quantity as described above.
  - Corrections, if any, shall be made by crossing out, initialing, dating and re writing.
  - All duties and other levies payable by the supplier under the contract shall be included in the unit price.
  - Applicable taxes shall be quoted separately for all items.
  - The prices quoted by the bidder shall be fixed for the duration of the contract and shall not be subject to adjustment on any account.
  - The Prices should be quoted in Indian Rupees only.
5. Each bidder shall submit only one quotation.
6. Quotation shall remain valid for a period not less than **40** days after the last date of quotation submission.
7. **The quotation should include the following information**
- Report on financial status (balance sheet and auditor's report for the past three year)
  - An affidavit for not been black listed by any Govt. Institution/Organization.

- iii) Authorization Certificate from the OEM/Principal (if bidder/supplier is not an OEM) assuring full guarantee and warranty obligations during the liability period, for the goods offered.
  - iv) The list of clients duly supported by copies of Purchase Orders, Installation and performance report signed by the purchasers/ users.
  - v) Details of service/support centers located in India.
8. Evaluation of Quotations.  
The Purchaser will evaluate and compare the quotations determined to be substantially responsive i.e. which
    - 8.1 are properly signed ; and
    - 8.2 Confirm to the terms and conditions, and specifications.
  9. The Quotations would be evaluated for all items together.
  10. Award of contract:  
The Purchaser will award the contract to the bidder whose quotation has been determined to be substantially responsive and who has offered the lowest evaluated quotation price.
    - 10.1 Notwithstanding the above, the Purchaser reserves the right to accept or reject any quotations and to cancel the bidding process and reject all quotations at any time prior to the award of contract.
    - 10.2 The bidder whose bid is accepted will be notified of the award of contract by the Purchaser prior to expiration of the quotation validity period. The terms of the accepted offer shall be incorporated in the purchase order.
  11. Payment shall be made in Indian Rupees as follows:  
**Satisfactory Acceptance-100% payment after Satisfactory Installation.**
  12. All supplied items are under warranty of **36 months (In case of software purchase the 36 months warranty include free of cost updations/ upgradations & Technical assistance)** from the date of successful acceptance of items.
  13. You are requested to provide your offer latest by **14:30 hours on 20 Feb., 2020**
  14. Detailed specifications of the items are at Annexure I.
  15. Training Clause (if any) **Training on operation and handling of equipment/ software ----- free of cost as per Department requirement.**
  16. Testing/Installation Clause (if any) **Full installation and testing/ demonstration ----- free of cost**
  17. Information brochures/ Product catalogue, if any must be accompanied with the quotation clearly indicating the model quoted for.
  18. Sealed quotation to be submitted/ delivered at the address mentioned below,

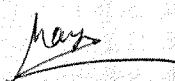
Dr. Mayur Agarwal/ Head ECE  
College of Technology  
GB Pant University of Agriculture & Technology  
Pantnagar- 263145 (Uttarakhand)  
Provide following information on the top of the envelope:

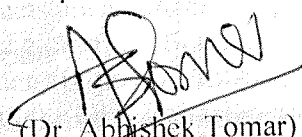
**Name of Equipment:**

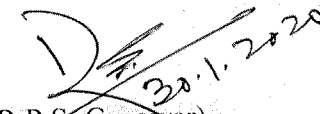
**Package Name:**

**Due Date of quotation closing:**

19. We look forward to receiving your quotation and thank you for your interest in this project.

  
(Dr. Mayur Agarwal)  
Assistant Prof., TEQIP  
ECE, CoT Pantnagar

  
(Dr. Abhishek Tomar)  
Associate Prof.  
ECE, CoT Pantnagar

  
(Dr. R.P.S. Gangwar)  
Head  
ECE, CoT Pantnagar

**Annexure I**

Sr. No	Item Name	Specifications
1	Vivado Design Suit	<p>Xilinx Vivado Software - latest version (V19.1 or later) with following specifications:</p> <ul style="list-style-type: none"><li>• Integrated Design Environment Software Development Kit (SDK)</li><li>• HDL Simulation</li><li>• Logic Analyser for FPGA</li><li>• Serial I/O Analyser</li><li>• Partial Reconfiguration</li><li>• Debug IP (ILA/VIO/IBERT)</li><li>• High-Level Synthesis<ul style="list-style-type: none"><li>• Abstraction of algorithmic description, data type specification (integer, fixed-point or floating-point) and interfaces (FIFO, AXI4, AXI4-Lite, AXI4-Stream)</li><li>• Extensive libraries for arbitrary precision data types, video, DSP.</li><li>• Directives driven architecture-aware synthesis that delivers the best possible QoR</li><li>• Fast time to QoR that rivals hand-coded RTL</li><li>• Accelerated verification using C/C++ test bench simulation, automatic VHDL or Verilog simulation and test bench generation</li><li>• Automatic use of on-chip memories, DSP elements and floating-point library</li></ul></li><li>• System Generator for DSP applications</li><li>• Software Defined System On Chip</li><li>• An Integrated Development Environment (IDE)</li><li>• A profiler to guide application optimization</li><li>• Compilers for host &amp; FPGA-accelerated code</li><li>• Emulation flows for rapid development and debug</li><li>• Automatic communication between software and hardware</li><li>• Latest Versions with perpetual license for all items</li><li>• Compatible with Windows 10</li><li>• Specifications must be complied by the respective OEM on his letter pad</li></ul>
2	Xilinx FPGA Kits	<p>FPGA Kit with following Specifications</p> <ul style="list-style-type: none"><li>• Ultra Scale+ MPSoC</li><li>• System Logic Cells – 600K, Memory – 32 Mb, DSP Slices – More than 2500, I/O Pins – More than 300</li></ul> <p><b>Configuration:</b></p> <ul style="list-style-type: none"><li>• Onboard JTAG configuration circuitry to enable configuration over USB</li><li>• Dual Quad-SPI flash memory, Boot from SD card</li></ul>

**Memory:**

- PS 4GB DDR4 64-bit SODIMM w/ ECC,
- PL 512MB DDR4 component memory ([256 Mb x 16] devices) at 1200MHz / 2400Mbps DDR,
- 8KB IIC EEPROM, Dual 64MB Quad SPI flash,
- SD card slot

**Control & I/O**

- 6x Directional Push Buttons (5x PL, 1x PS)
- DIP switches (8x PL)
- PMBUS & System Controller MSP430 for power, clocks, and I<sup>2</sup>C bus switching
- USB2/3 (MIO ULPI and 1 GTR)

**Expansion Connectors:**

- 2x FMC-HPC connectors (16 GTH Transceivers, 64 differential user defined signals),
- 2x PMOD headers, IIC

**Communication & Networking**

- RGMII communications at 10, 100, or 1000 Mb/s. Serial GMII interface supports a 1 Gb/s SGMII interface
- 4x SFP+ cage
- SMA GTH support (4x SMA Tx/Rx connectors)
- UART To USB bridge
- RJ45 Ethernet connector
- SATA (1 x GTR)
- PCIe Gen 2x4 Root Port

**Display**

- HDMI video input and output (3 GTH)
- External Retimer device driving an HDMI output connector
- 9x GPIO user LEDs (8x PL, 1x PS)
- VESA Display Port 1.2 source-only controller supports up to two lanes of main link data at rates of 1.62 Gb/s, 2.70 Gb/s, or 5.40 Gb/s.

Specifications must be complied by the respective OEM on his letter pad

# **FORMAT FOR QUOTATION SUBMISSION**

(In letterhead of the supplier with seal)

To: \_\_\_\_\_

Date: \_\_\_\_\_

Sl. No.	Description of goods (with full Specifications)	Qty.	Unit	Quoted Unit rate in Rs. (Including Ex Factory price, excise duty, packing and forwarding, transportation, insurance, other local costs incidental to delivery and warranty/ guaranty commitments)	Total Price (A)	Sales tax and other taxes payable	
						In %	In figures (B)
				<b>Total Cost</b>			

Gross Total Cost (A+B): Rs. \_\_\_\_\_

We agree to supply the above goods in accordance with the technical specifications for a total contract price of Rs. \_\_\_\_\_ (Amount in figures) (Rupees \_\_\_\_\_ amount in words) within the period specified in the Invitation for Quotations.

We confirm that the normal commercial warranty/ guarantee of \_\_\_\_\_ months shall apply to the offered items and we also confirm to agree with terms and conditions as mentioned in the Invitation Letter.

We hereby certify that we have taken steps to ensure that no person acting for us or on our behalf will engage in bribery.

Signature of Supplier

Name: \_\_\_\_\_

Address: \_\_\_\_\_

Contact No: \_\_\_\_\_